

**ABSTRACT OF THE DISCLOSURE**

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2 A display device with a power interruption delay function, comprising a power interruption  
3 delay charging circuit for gradually lowering an input voltage to an horizontal/vertical processor  
4 constant voltage circuit when power supply to the display device is interrupted. According to the  
5 present invention, when power supply is resumed under the condition that a high voltage charged  
6 on a horizontal deflection coil and an S-correction capacitor is not sufficiently discharged after  
7 power interruption, the power interruption delay charging circuit can prevent a horizontal output  
8 transistor from being damaged due to an instantaneous surge current resulting from the high voltage  
9 being not discharged.

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